

TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

Title of Invention	METHOD OF INTEGRATING HIGH VOLTAGE METAL OXIDE SEMICONDUCTOR DEVICES AND SUBMICRON METAL OXIDE SEMICONDUCTOR DEVICES
Application Number :	
Date :	
First Named Applicant:	Ching-Chun Huang
Confirmation Number:	
Attorney Docket Number:	NAUP0523USA
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Submitted By:	Elec. Sign.
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Documents being submitted:

	Files
us-assignment	NAUP0523-usassn.xml us-assignment.xsl us-assignment.dtd NAUP0523ASS1.tif NAUP0523ASS2.tif
us-request	NAUP0523-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	NAUP0523-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
us-declaration	NAUP0523-usdecl.xml us-declaration.dtd us-declaration.xsl
us-power-of-attorney-grant	NAUP0523-uspoat.xml us-power-of-attorney-grant.dtd us-power-of-attorney-grant.xsl
application-body	NAUP0523-trans.xml us-application-body.xsl application-body.dtd wipo.ent mathml2.dtd mathml2-qname-1.mod isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isogr3.ent isomfrk.ent isomopf.ent isomscr.ent isotech.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isolat1.ent isolat2.ent isonum.ent isopub.ent mmlextra.ent mmlalias.ent soextblk.dtd Naup0523usa-01.tif Naup0523usa-02.tif Naup0523usa-03.tif Naup0523usa-04.tif

us-declaration
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Naup0523usa-05.tif
Naup0523usa-06.tif
Naup0523usa-07.tif
Naup0523usa-08.tif
Naup0523usa-09.tif
Naup0523usa-10.tif
NAUP0523DEC1.tif
NAUP0523DEC2.tif

Comments